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174/161 Cont.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Stephen J. Smith et al.
Application No. : 10/666,948
Confirmation No. : 7049
Filed : September 19, 2003
For : RECONFIGURABLE PROGRAMMABLE LOGIC
DEVICE COMPUTER SYSTEM
Art Unit : 2115
Examiner : Suresh Suryawanshi

New York, New York 10020
December 22, 2005

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, applicants
hereby make the following documents of record in the above-
identified patent application:

U.S. PATENT DOCUMENTS

5,541,849	Rostoker et al.	07-30-1996
5,819,064	Razdan et al.	10-06-1998
6,044,211	Jain	03-28-2000
6,226,776	Panchul et al.	05-01-2001
6,457,173	Gupta et al.	09-24-2002

12/28/2005 HLE333 00000050 061075 10666948

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6,477,683	Killian et al.	11-05-2002
6,608,638	Kodosky et al.	08-19-2003
6,625,797	Edwards et al.	09-23-2003
6,745,160	Ashar et al.	06-01-2004

FOREIGN PATENT DOCUMENT

*WO 97/09930	PCT	03-20-1997
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NON-PATENT LITERATURE DOCUMENTS

*IBM, "Programmable Manual Cable Assembly Board," IBM Technical Disclosure Bulletin, Vol. 31, No. 12, May 1989, pp. 306-309.

*Nanya, "Asynchronous VSLI System Design," ASP-DAC '98 Tutorials, February 10, 1998, Yokohama, Japan.

*Nanya et al. "Scalable-Delay-Insensitive Design: A high-performance approach to dependable asynchronous systems," Proceedings of International Symposium on Future of Intellectual Integrated Electronics, March, 1999, pp. 1-10.

These documents are listed on the accompanying Form PTO/SB/08 (submitted in duplicate). The aforementioned U.S. Patent Documents fall under 37 C.F.R. § 1.98 (a)(2) for which documents the requirement to provide copies is waived. Accordingly, no copies of the aforementioned U.S. Patent Documents are provided. Copies of the Foreign Patent Document and Non-Patent Literature Documents are enclosed herewith.

It is respectfully requested that these documents be (1) fully considered by the U.S. Patent and Trademark

Office during the examination of this application;
and (2) printed on any patent which may issue on this
application. Applicants request that a copy of Form
PTO/SB/08, as considered and initialed by the Examiner, be
returned with the next communication.

Consideration of the foregoing in relation to this
patent application is respectfully requested.

Respectfully submitted,

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**SUPPLEMENTAL INFORMATION
DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

Application Number	10/666,948
Filing Date	September 19, 2003
First Named Inventor	Stephen J. Smith
Art Unit	2115
Examiner Name	Suresh Suryawanshi
Attorney Docket Number	174/161 Cont.
Confirmation Number	7049

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		5,541,849	07-30-1996	Rostoker et al.	
		5,819,064	10-06-1998	Razdan et al.	
		6,044,211	03-28-2000	Jain	
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		WO 97/09930	03-20-1997	Medison Co., Ltd.		

NON PATENT LITERATURE DOCUMENTS

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		Nanya, "Asynchronous VSLI System Design," ASP-DAC '98 Tutorials, February 10, 1998, Yokohama, Japan.

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Sheet 2 of 2

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